

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : DOWLING, Eric M.)
App. No. : 09/021,933)
Filed : February 11, 1998)
For : EMBEDDED-DRAM-DSP)
ARCHITECTURE)
Examiner : Kim, Hong)
_____)

ESTABLISHMENT OF RIGHT OF ASSIGNEE TO TAKE ACTION
AND
REVOCATION AND POWER OF ATTORNEY

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

The undersigned is empowered to act on behalf of the assignee below (the "Assignee"). The original assignment is recorded at Reel 9009 and Frame 0502. This Assignment represents the entire chain of title of this invention from the Inventor to the Assignee.

I declare that all statements made herein are true, and that all statements made upon information and belief are believed to be true, and further, that these statements were made with the knowledge that willful, false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001, and that willful, false statements may jeopardize the validity of the application, or any patent issuing thereon.

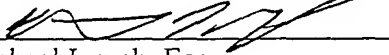
The undersigned hereby revokes any previous powers of attorney in the subject application, and hereby appoints the registrants of GAZDZINSKI & ASSOCIATES, 3636 Nobel Drive, Suite 215, San Diego, California 92122, Telephone (858) 320-2030, including Robert F. Gazdzinski, Registration Number 39,990, as its attorneys with full power of substitution and revocation to prosecute this application and to transact all business in the U.S. Patent and

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Trademark Office connected herewith. This appointment is to be to the exclusion of the inventor and his attorney(s) in accordance with the provisions of 37 C.F.R. § 3.71.

MICRON TECHNOLOGY, INC.

Dated: 3-31-98

By: 
Michael Lynch, Esq.

Title: Chief Patent Counsel

Address: 8000 South Federal Way
Boise, Idaho 83707-0006

DECLARATION - USA PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled EMBEDDED-DRAM-DSP ARCHITECTURE; the specification of which is attached hereto;

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above;

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56;

I hereby claim the benefit under Title 35, United States Codes § 119(e) of any United States provisional application(s) listed below.

Application No. 60/054,439 Filing Date: August 1, 1997

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful, false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole inventor: Eric M. Dowling

Inventor's signature Eric M. Dowling

Date 24 Jan 98

Residence: 1132 W. Lookout Drive, Richardson, Texas 75080

Citizenship: United States

Post Office Address: Same as residence

Send Correspondence To:
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Customer No. 20,995

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